



VePAL TX150+

Handheld SONET Test Set



SONET network testing simplified

VeEX™ VePAL TX150+ is a next generation test solution for SONET, DS3/DS1 networks transporting legacy and next generation services.

Platform Highlights

- Intuitive presentation of measurements with test graphics
- High resolution color touch-screen viewable in any lighting conditions fitted with protective cover
- Robust, handheld chassis packed with powerful and flexible features for demanding environments and test conditions
- Optimized for field engineers or technicians installing and maintaining SONET networks transporting legacy and next generation Ethernet services
- Ethernet port and connection for back office applications, workforce management and triple play service verification
- User defined test profiles and thresholds enable fast, efficient and consistent turn-up of services
- USB memory stick support and FTP upload capability for test result storage and file transfer respectively
- Maintain instrument software, manage test configurations, process measurement results and generate customer test reports using included ReVeal™ PC software
- Extend field testing time using interchangeable Lilon battery pack/s
- Supports advanced IP testing; Ping, Trace route, ARP Wiz, IPTV, Web browser, and FTP upload/download via Ethernet or USB port where applicable
- Perform remote testing and monitoring using the remote control option via standard Ethernet interface

Key Features

- Optical SONET/SDH testing for OC-1/3/12/48; also supports STS-1, STS-3 Electrical
- Supports DS1, DS3 bit rates; PDH E1, E2, E3, E4 Optional
- Balanced (100Ω) interface for DS1
- Dual DS1, E1 and DS3 Rx BERT
- Full Rate DS1, E1, Fractional N, Nx64 kbps or Nx56 kbps testing
- Non-intrusive Pulse Mask Analysis at DS1, E1, E3, DS3 bit rates
- Flexible wavelength/bit rate options using industry standard SFPs conforming to the Multi Source Agreement (MSA)
- Optical Power, Level and Frequency measurements
- Auto Configuration of network type, bit rate, line coding, framing, mapping and test pattern
- Payload Mapping according to Bellcore GR.253
- Concatenated Payloads
- Bit Error and Performance Analysis per ITU/Bellcore standards
- Error and Alarm Generation and Analysis
- Path Trace Generation and Analysis
- Pointer Generation and Analysis
- Automatic Protection Switching/Service Disruption testing
- Histogram and Event analysis for errors and alarms
- Round Trip Delay on all interfaces and payload mappings
- Section/Line and Path Overhead Monitoring, Byte Decoding
- Tandem Connection Monitoring
- DS1 and DS3 Loop capability
- Jitter analysis at E1/DS1/E3/DS3 and OC-3 (STM-10)